

Figure 1

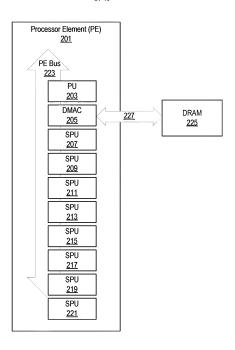


Figure 2

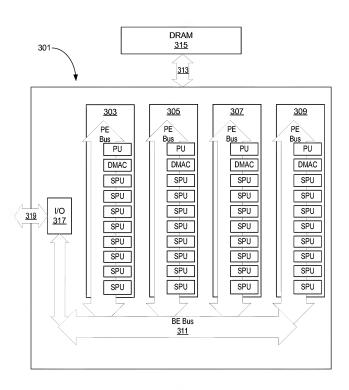


Figure 3

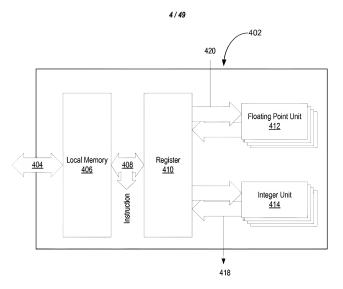


Figure 4

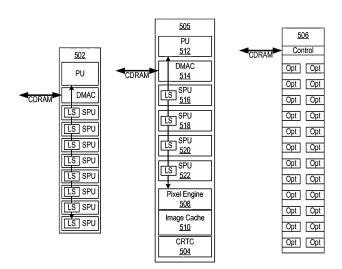
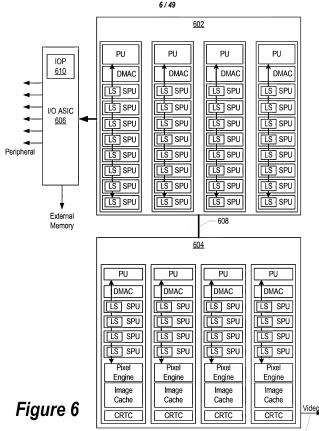


Figure 5

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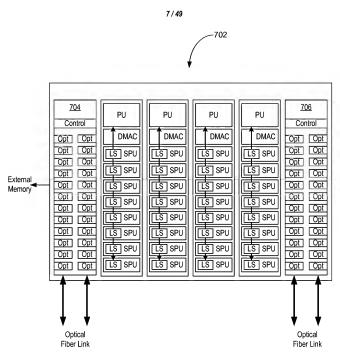


Figure 7

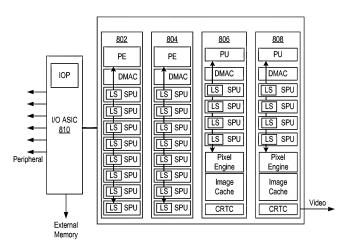


Figure 8

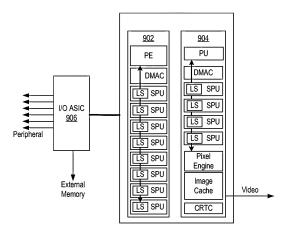


Figure 9

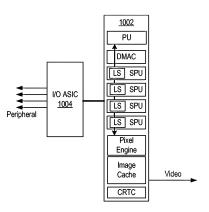
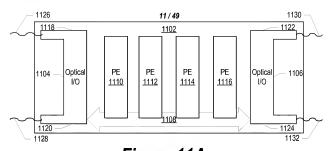
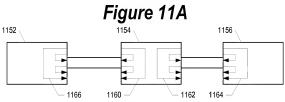
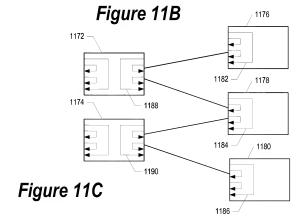


Figure 10

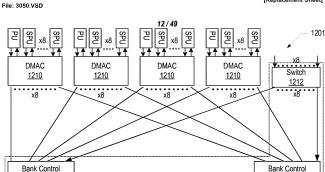




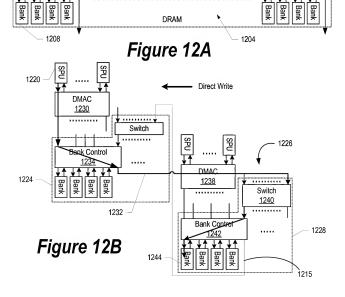


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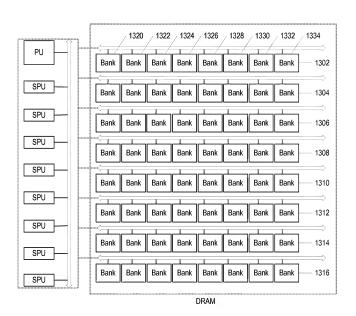


Figure 13

File: 3050.VSD [Replacement Sheet]



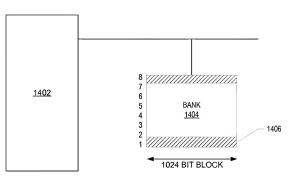


Figure 14A

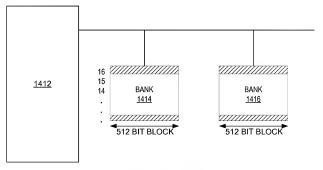


Figure 14B



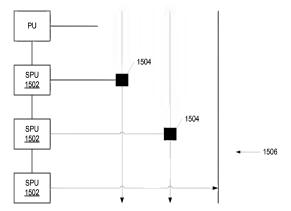


Figure 15

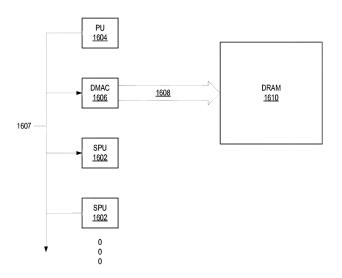
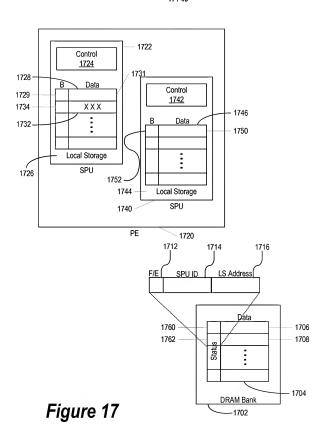


Figure 16

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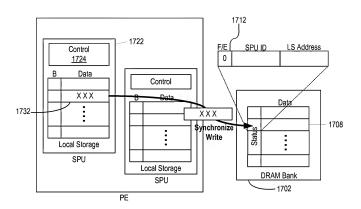


Figure 18

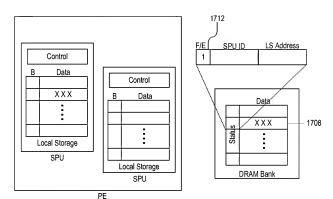
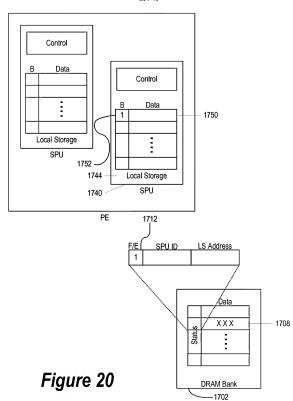


Figure 19

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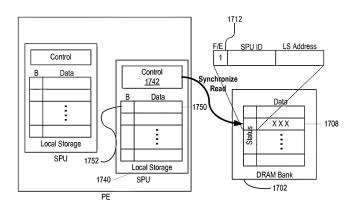
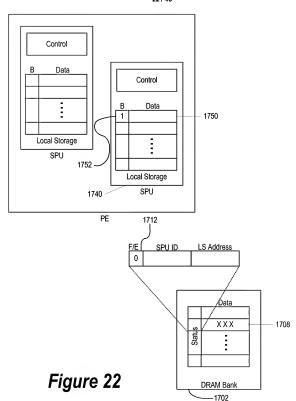


Figure 21

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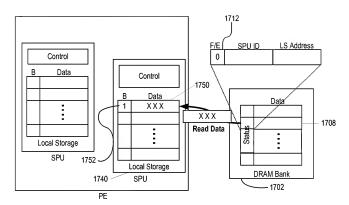
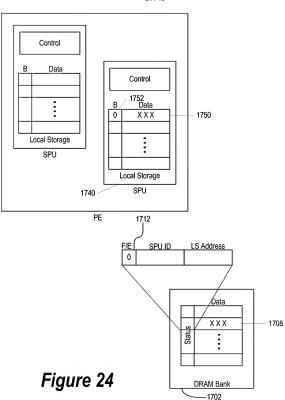
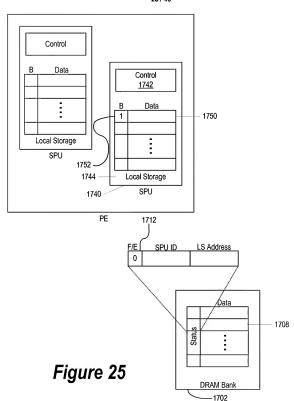


Figure 23

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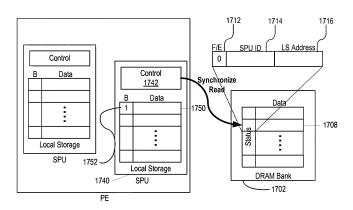


Figure 26

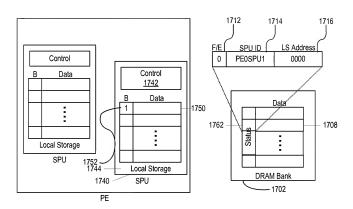


Figure 27

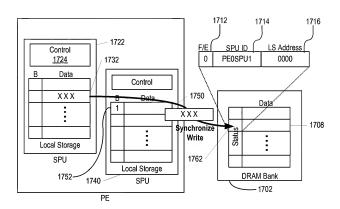


Figure 28

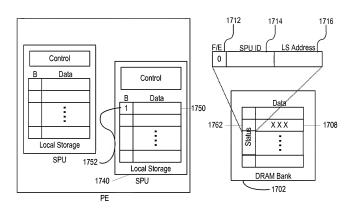


Figure 29

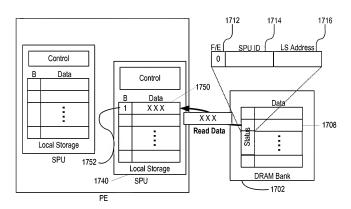


Figure 30

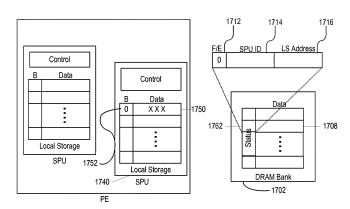


Figure 31

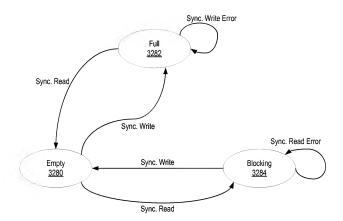


Figure 32

File: 3050.VSD [Replacement Sheet]

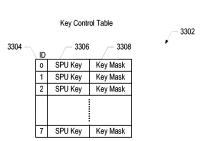


Figure 33

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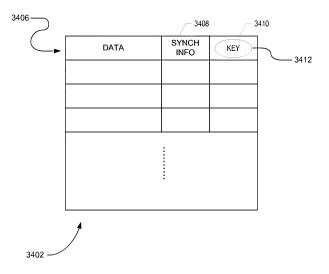


Figure 34

File: 3050.VSD [Replacement Sheet]

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Memory Access Control Table

		₂ — 3506	_ 35	08 3510	3512
3504 —	ID	1			
	0	Base	Size	Access Key	Access Key Mask
	1	Base	Size	Access Key	Access Key Mask
	2	Base	Size	Access Key	Access Key Mask
	63	Base	Size	Access Key	Access Key Mask



Figure 35



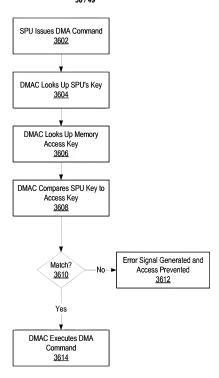


Figure 36

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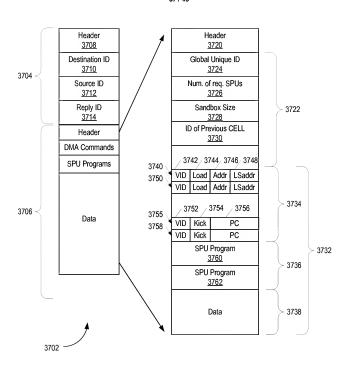


Figure 37

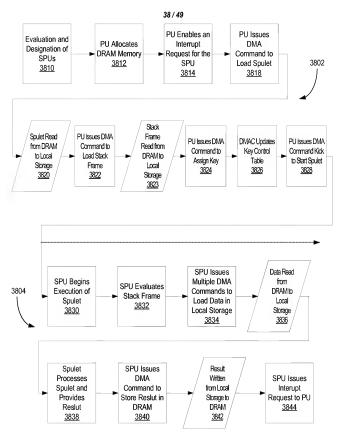
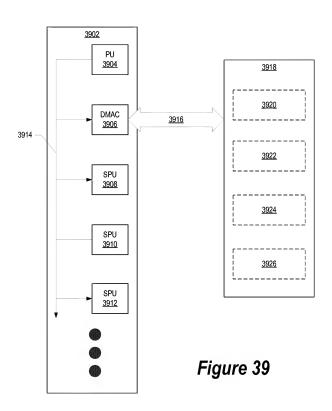


Figure 38

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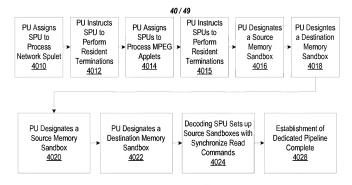


Figure 40A

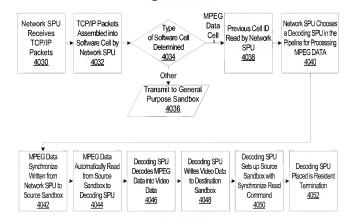


Figure 40B

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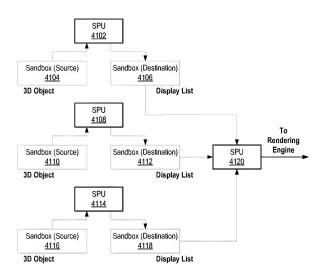


Figure 41

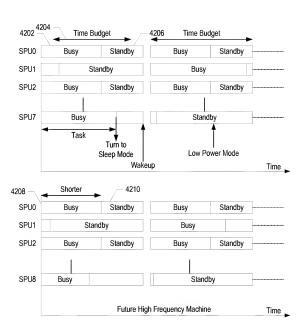
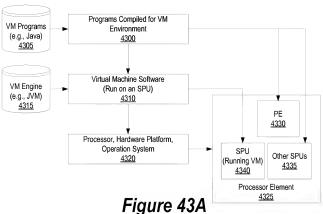


Figure 42





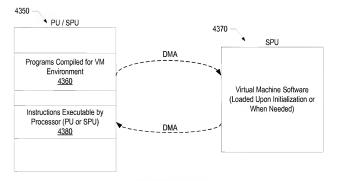


Figure 43B

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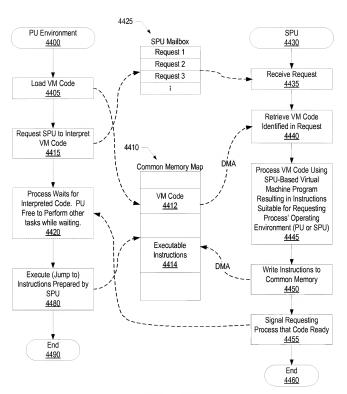
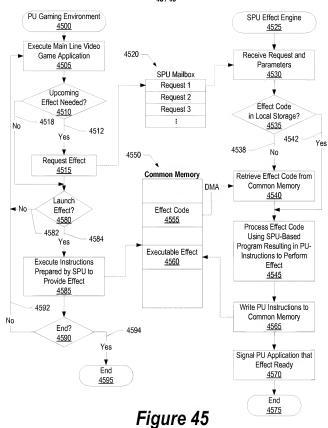
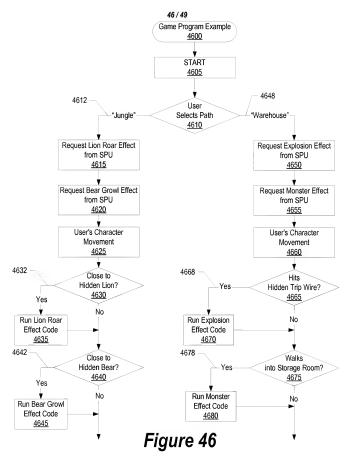


Figure 44







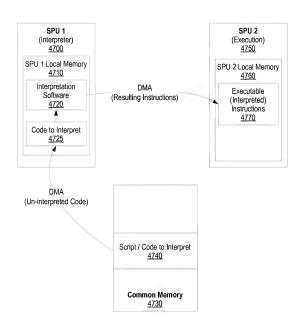


Figure 47

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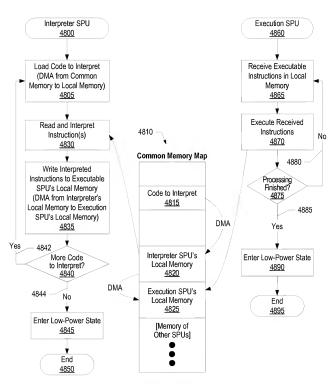


Figure 48

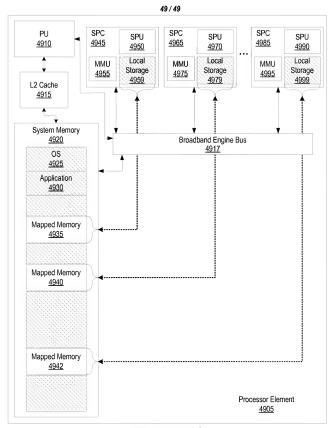


Figure 49